

FACULTY OF ENGINEERING

B. E. (CSE) (CBCS) V – Semester (Backlog) Examination, October 2020

Subject: Data Communications

Time: 2 Hours

PART –A

Max. Marks :70

(5x2=10 Marks)

Note : Answer any Five Questions

1. What are the major components of Data communication system?
2. Describe attenuation.
3. Write about Frame relay.
4. Define Line coding.
5. Distinguish between Datagram and Virtual circuit operation.
6. Define single bit errors and burst errors.
7. Explain the concept of Parity check and CRC in error detection.
8. What are the functions of physical layer?
9. What is CSMA/CD?
10. Describe the merits and demerits of different LAN technologies.

PART-B

Note : Answer any Four Questions

(4x15 = 60 Marks)

11. (a) Explain in detail about ATM protocol architecture.
(b) Explain about HEC operation at receiver.
12. (a) Describe the formula of shanon for channel capacity.
(b) For the bit stream 11001010 sketches the wave form of any three digital encoding formats.
13. (a) Write short notes on stop and wait ARQ.
(b) Discuss briefly about HDLC Data link control protocol.
14. (a) What are IEEE standards? Explain any one.
(b) Discuss MAC Sub layer
15. (a) What are the different types of Ethernet? Describe the characteristics for one of the Ethernet types.
(b) Elaborate spanning Tree Bridge
16. (a) Explain Bluetooth architecture.
(b) Briefly describe ATM call format.
17. (a) Distinguish Synchronous & Asynchronous transmission.
(b) Write short notes on error correction techniques.

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