

Code No.: 5181/O

FACULTY OF ENGINEERING B.E. 2/4 (M/P) II Semester (Old) Examination, May/June 2012 BASIC ELECTRONICS

Time: 3 Hours]

[Max. Marks: 75

Note: Answer **all** questions from Part – **A**. Answer **any five** questions from Part – **B**.

	PART-A (25 Ma	arks)
4.	Differentiate between intrinsic and extrinsic semiconductors.	3
2.	i) Mobilityii) Drift current.	2
3.	Why is the FET called a voltage controlled device? Explain.	3
4.	Compare the 3 configurations of the transistor amplifier.	3
5.	Draw the circuit for zener diode regulator.	3
6.	Write the condition for sustained oscillations of Hartley oscillator.	2
7.	What is the advantage of crystal oscillator?	2
8.	Write DeMorgans theorems.	3
9.	Draw the symbols for photo diode, LED, SCR and UJT.	2
10.	Explain two applications of CRO.	2
	PART-B (50 Ma	arks)
American s	a) How is a p-n junction formed? Draw the circuit diagram of p-n junction diode in forward bias and reverse bias. Explain its operation and give V-I characteristics.	6
	b) What is a rectifier ? Draw the circuit diagram for bridge rectifier with LC π filter and explain its operation.	4

